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				Application Number	09/535,015
				Filing Date	March 24, 2000
				First Named Inventor	Shunpei YAMAZAKI et al.
				Group Art Unit	2811
				Examiner Name	S. Crane
Sheet	1	of	1	Attorney Docket Number:	0756-2131

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U.S. PATENT DOCUMENTS

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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

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SiSC		Wolf, <i>Silicon Processing for the VLSI ERA</i> , Volume 2: Process Integration, Page 274 (1990)	

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